VESTEL BUS AUDIO IN

PL703 PL705 PL702

C748 100p 50V

C704 56p 50V

R725 1k

C700 10p 50V C701 10p 50V X700 18.432MHz

C702 47p 50V C703 47p 50V

L700 C708 22n 50V C706 10u 50V

C747 47p 50V C712 10u 25V C713 100n 25V

R726 1k C714 100n 25V SC1_AIR

R727 1k C716 100n 25V SC1_AIL

R728 1k C717 100n 25V SC2_AIR

R729 1k C718 100n 25V SC2_AIL

R730 1k C721 100n 25V FR/BK_A_I_R

R731 1k C723 100n 25V FR/BK_A_I_L

R718 1k C726 100n 25V

R720 1k C727 100n 25V

C729 100n 25V C730 3u3 50V

L702 22u +5V

C734 50V 10u C735 22n 50V

C733 50V 10u C736 50V 10u

R721 100R C737 470n 63V SC1_AOL

R722 100R C738 470n 63V SC1_AOR

R723 100R C739 470n 63V SC2_AOL

R724 100R C740 470n 63V SC2_AOR

IC700 MSP3410D_DIP

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
AUD_CL_OUT	NC1	NC2	DCTR_OUT1	DCTR_OUT0	ADR_SEL	STBYQ	NC3	I2C_CL	I2C_DA	I2S_CL	I2S_WS	I2C_DAOUT	I2C_DAIN1	ADR_DA	ADR_WS	ADR_CL	DVSUP	DVSS	I2C_DAIN2	NC4	NC5	NC6	RESETQ	DACA_R	DACA_L	VREF2	DACM_R	DACM_L	NC7	DACM_SUB	NC8
NC9	XTAL_OUT	XTAL_IN	TESTEN	ANA_IN2+	ANA_IN-	ANA_IN1+	AVSUP	AVSS	MONO_IN	VREFTOP	SC1_IN_R	SC1_IN_L	AGS1	SC2_IN_R	SC2_IN_L	AGS2	SC3_IN_R	SC3_IN_L	AGS4	SC4_IN_R	SC4_IN_L	AGNDC	AHVSS	CAPL_M	AHVSUP	CAPL_A	VREF1	SC1_OUT_L	SC1_OUT_R	SC2_OUT_L	SC2_OUT_R	
64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33

R701 100R +5V C705 50V 10u PL704 PL706

C707 50V 47p R703 100R SCL

C710 47p 50V R705 100R SDA

C719 50V 22n C720 10u 50V

L701 BLM21B201S +5V

BC858B Q700 R715 470R R719 1k

D701 3V6 1N4148 D700

C724 10u 50V R717 470R

C744 1n 50V C743 1n 50V

L703 22u C742 2n2 50V MOUT_R

L704 22u C741 2n2 50V MOUT_L

PL700 C745 4n7 50V C746 4n7 50V

IC701 TDA1308

4	VSS	INB+	5
3	INA+	INB-	6
2	INA-	OUTB	7
1	OUTA	VDD	8

C722 16V 100u R702 10k R704 10k C711 1u 50V R706 10k C715 100u 16V

R713 10k R716 4k7 C731 25V 100n

R709 4k7 C732 25V 100n

C725 10u 50V +5V

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Ver.	Rev.	DATE	Author	Sheet
A11		14/05/2003	Y. ELIK	07 OF 07